FIG. 1

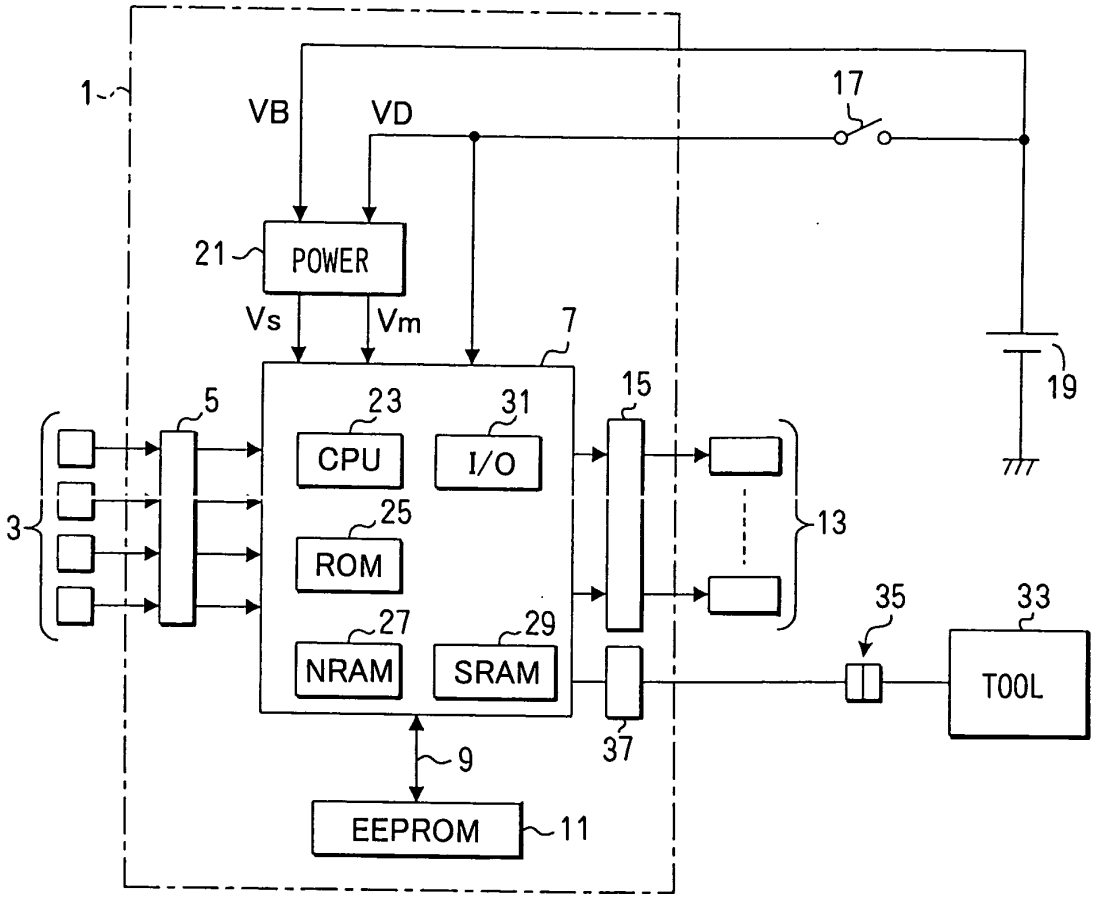

1
VB
VD
17
21
POWER
Vs
Vm
7
19
5
23
31
15
CPU
I/O
3
25
ROM
13
27
29
35
33
NRAM
SRAM
TOOL
37
9
EEPROM
11

FIG. 2A

	"CST DATA"
i=0	EVAPO NUM
i=1	EVAPO DEN
i=2	CATAL NUM
i=3	CATAL DEN
i=4	SENSOR NUM
i=5	SENSOR DEN

FIG. 2B

	"END FLAG"
i=0	F[0]
i=1	F[1]
i=2	F[2]
i=3	F[3]
i=4	F[4]
i=5	F[5]

FIG. 3

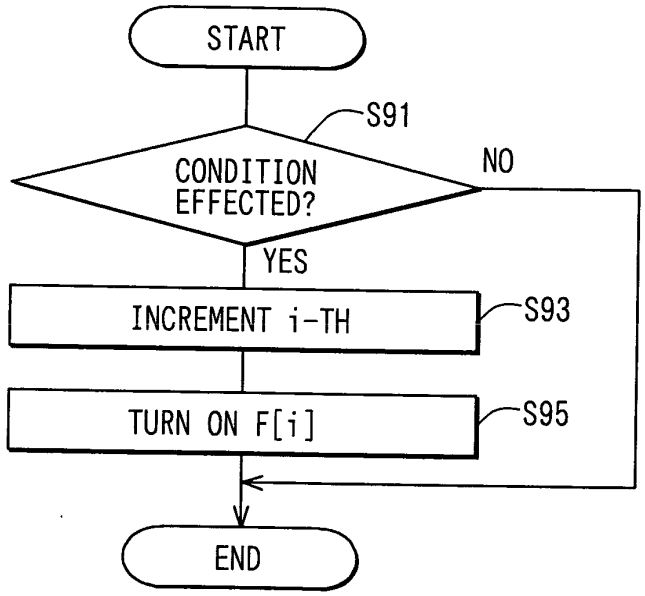


FIG. 4

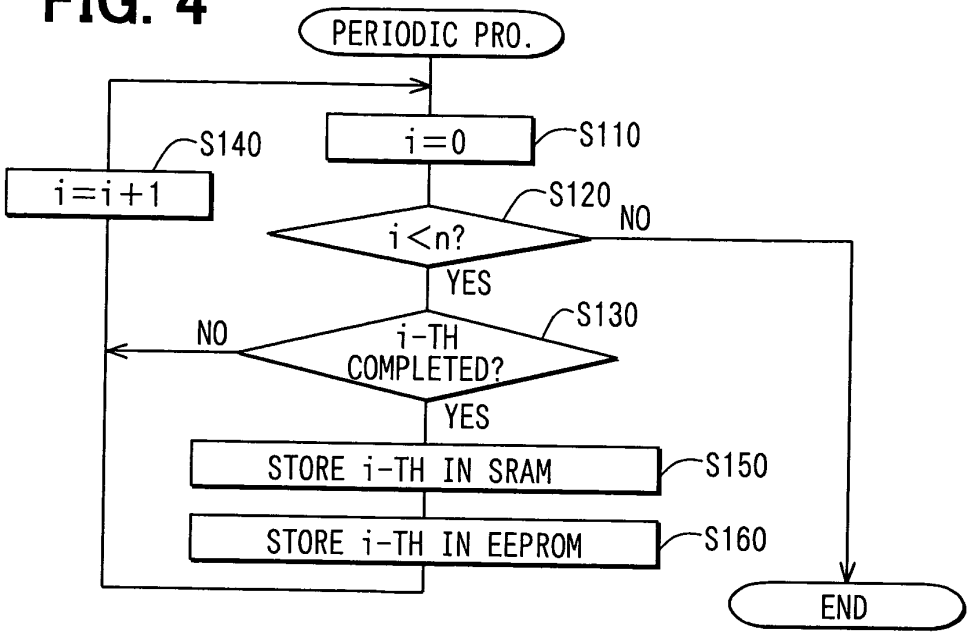

PERIODIC PRO.
i=0
S110
S120
i<n?
NO
YES
S130
i-TH COMPLETED?
NO
YES
STORE i-TH IN SRAM
S150
STORE i-TH IN EEPROM
S160
i=i+1
S140
END

FIG. 5

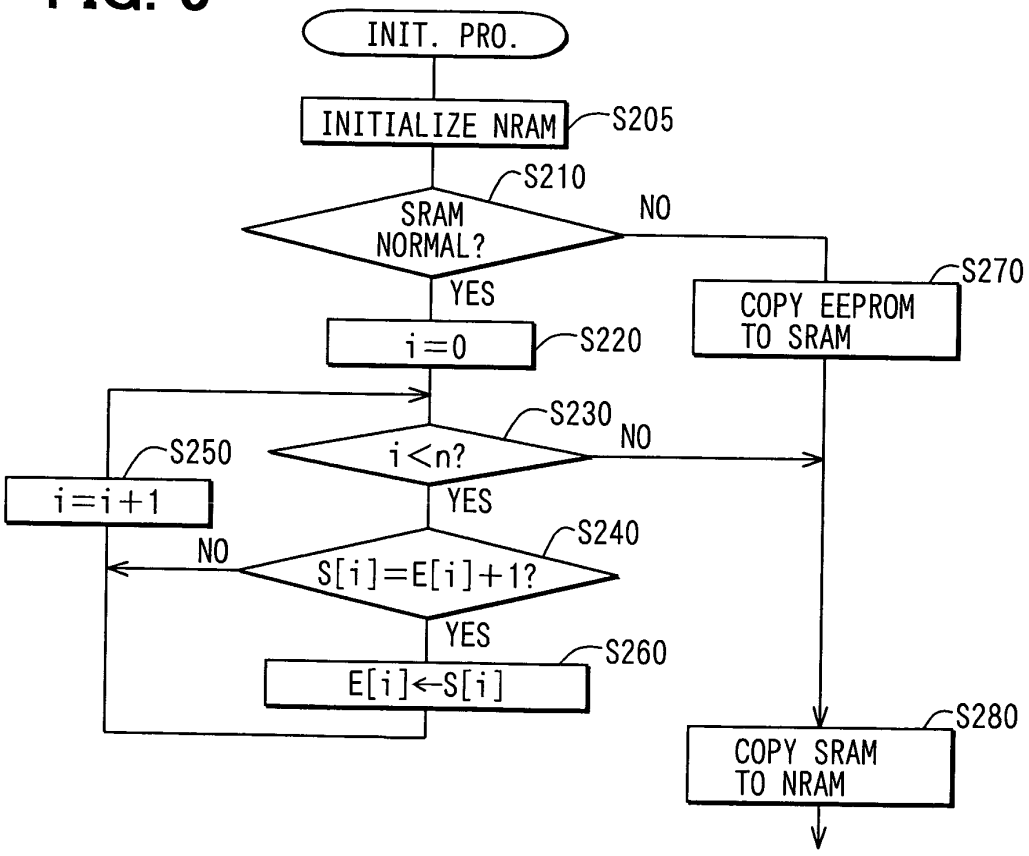

INIT. PRO.
INITIALIZE NRAM
S205
S210
SRAM NORMAL?
NO
YES
S270
COPY EEPROM TO SRAM
i=0
S220
S230
i<n?
NO
YES
S250
i=i+1
S240
S[i]=E[i]+1?
NO
YES
S260
E[i]←S[i]
S280
COPY SRAM TO NRAM

FIG. 6

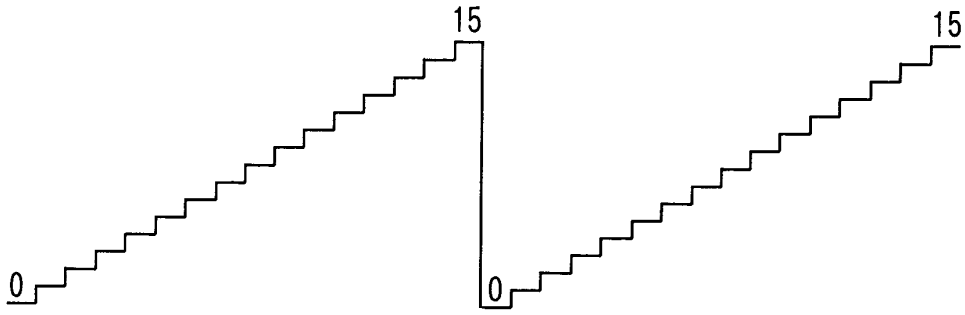
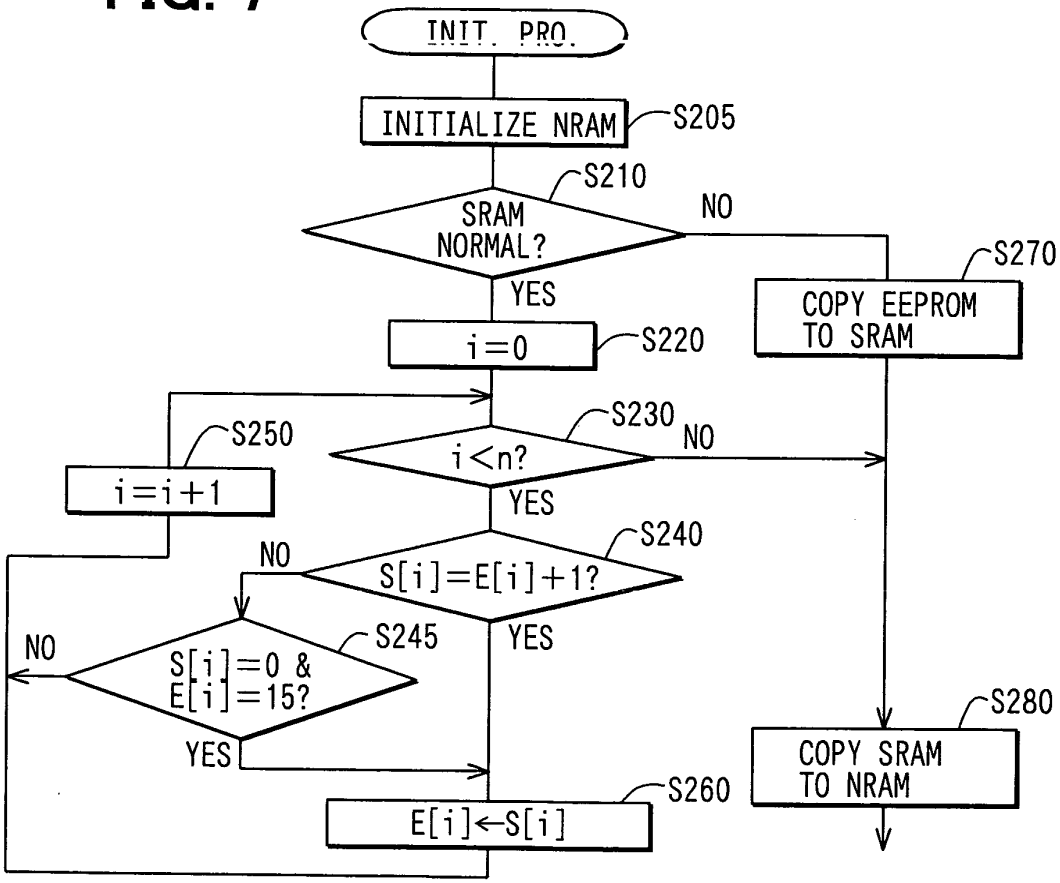

15
15
0
0

FIG. 7


INIT. PRO.
INITIALIZE NRAM
S205
S210
SRAM NORMAL?
NO
YES
S270
COPY EEPROM TO SRAM
i=0
S220
S230
i<n?
NO
YES
S250
i=i+1
S240
NO
S[i]=E[i]+1?
YES
NO
S245
S[i]=0 & E[i]=15?
YES
S280
COPY SRAM TO NRAM
S260
E[i]←S[i]